

Sr. No.	Name of the Student Seat No.		ECC501			ECC502			ECC503				ECC504			ECCDLO5014			ECL501			ECL502			ECL503			ECL504			ECLDLO5014			Outof 750					
			Microprocessor & Peripherals Interfacing			Digital Communication			Electromagnetic Engineering				Discrete Time Signal Processing			Data Compression And Encryption			Microprocessor & Peripherals Interfacing Lab			Digital Communication Lab			Business Communication & Ethics Lab			Open Source Technology For Communication Lab			Data Compression And Encryption Lab I								
			Maximum	SE 80 32	IA 20 8	TOT 100 40	SE 80 32	IA 20 8	TOT 100 40	SE 80 32	IA 20 8	TOT 100 40	TW 25 10	SE 80 32	IA 20 8	TOT 100 40	SE 80 32	IA 20 8	TOT 100 40	TW 25 10	O/P 25 10	TOT 50 20	TW 25 10	O/P 25 10	TOT 50 20	TW 50 20			TW 25 10	O/P 25 10	TOT 50 20	TW 25 10							
EC	ECG	GPA																																					
1	/PATHAK RUTUJA RAJESH UJWALA 5223201	Marks Obtained	45+	13+	58	34+	11+	45	37+	14+	51	16+	42	08+	50	58+	18+	76	14+	13+	27	14+	17+	31	25+			16+	16+	32	11+			422					
		Grade	D	C	D	P	D	E	E	B	D	C	D	P	D	B	O	A	D	D	D	D	C	C	D			C	C	C	P			P					
		Grade Pt.			6			5			6	7			6			9			6			7	6					7	4								
		Credit Pt.			4			4			4	1			4			4			1			1	2					1	1			27					
		CP*GP			24			20			24	7			24			36			6			7	12					7	4			171					
																																							6.33

