



Parshvanath Charitable Trust's  
**A. P. SHAH INSTITUTE OF TECHNOLOGY**  
(Approved by AICTE New Delhi & Govt. of Maharashtra, Affiliated to University of Mumbai)  
(Religious Jain Minority)

**EXAMINATION TIME TABLE (APRIL 2021)**

**PROGRAMME - S.E. (Electronics & Telecommunication) (REV. -2019 'C' Scheme) (Choice Based)**

**SEMESTER - III (DSE)**

Days and Dates	Time	Course Code	Paper
10 April 2021	02:30 p.m. to 04:30 p.m.	ECC301	ENGINEERING MATHEMATICS-III
12 April 2021	02:30 p.m. to 04:30 p.m.	ECC302	ELECTRONIC DEVICES & CIRCUITS
15 April 2021	02:30 p.m. to 04:30 p.m.	ECC303	DIGITAL SYSTEM DESIGN
16 April 2021	02:30 p.m. to 04:30 p.m.	ECC304	NETWORK THEORY
17 April 2021	02:30 p.m. to 04:30 p.m.	ECC305	ELECTRONIC INTSTRUMENTATION & CONTROL SYSTEMS

**Important Note:** • Change if any, in the timetable shall be communicated on the college web site.

**Mumbai**  
**25th March, 2021.**



**Principal**